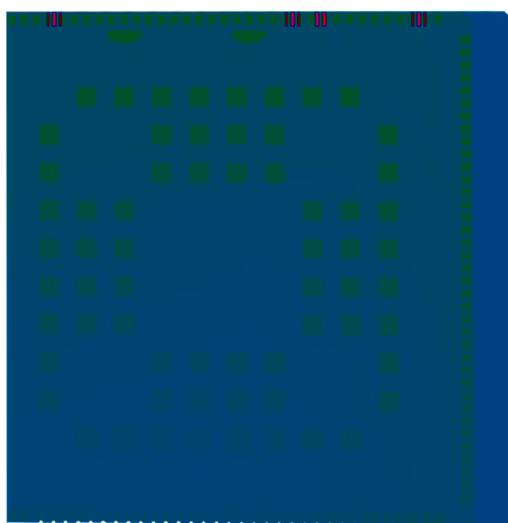


# Q ‡?ô IB\$ >



x · & =0	Q ‡?ô IB\$ >
x(x \ ')	
3 j ´	\$! c 25¾   a7- ° _6Ñ - 9L€ œ (
3 O	α 8

# x ñ ²F

\_0!n -‘µ^j -é ,’~f?JQ ¢7-FJ Q‡È” Q‡ F2  
+X ¾ J/ý5•5 f?,’íV  
¢7- 4iFJ Q‡Ä G÷+X ý OE3+5 È \_1,’ Ö •FO)· Ö

Eî  
Eî  
Eî  
Eî

X EQ FO í V ž Ö •,’ < & È ÈB N# Ä ÄFJAß2o Ä Ä

È `jJ ð¹Lb# Eœ&

(© W G>~

x ñ(©W	yF
£	
3+5 μ ^	~ é
ý Œ3+5	
j (	ÈFp~ „ 1>ô ÅG !:-
5•5 NÁ!â	
	_ 1
ž Œ•	
	ü ' ü μ _ 1 "
	ö

Ä }¼ > ü tÿ d Ä	t ÿ d }5ž >5ž	
EÄ •Ai 7	9K^ Ä 0 £ jK^ ÄN#Gÿ ÄN#Gÿ Ä	
	+e é ?	
	_ 1.œ = }	
Ä+X Ö	Ö =0	k?± İ7- ÿF
		Q †+e\$ÄEÄ • È È 70 l
		È 0 W _ 1
		_ 1 Ä : ù j f E , È+X ¾3W
		U ;E- Ä
		04ô 4ï È 04ô 4ï
		_ 1
	u+e İ7-	_ 1 FG u+e8ß(w
	PœEi	_ 1
		Î & Jİ > 7+e"•
4NÄ Ö	Fp/ „	
	D ...1 MŽ.u D 6c j D } · Ä V İ n Ä D \1, D 0û f 6c j	

Õ

36	UART_CTS	B-PD:nppukp	Configurable I/O UARTor I2C SDA
37	UART_RTS	B;B-PD:nppukp	Configurable I/O UARTor I2C SCL
38	VREG_L11_SDC	PO	PMIC output 2.95V
39	SDC2_SDCARD_CLK	BH-NP:pdpukp	Secure digital controller 2 clock
40	SDC2_SDCARD_CMD	BH-PD:nppukp	Secure digital controller 2 command
41	SDC2_SDCARD_D0	BH-PD:nppukp	Secure digital controller 2 data bit 0
42	SDC2_SDCARD_D1	BH-PD:nppukp	Secure digital controller 2 data bit 1
43	SDC2_SDCARD_D2	BH-PD:nppukp	Secure digital controller 2 data bit 2
44	SDC2_SDCARD_D3	BH-PD:nppukp	Secure digital controller 2 data bit 3
45	SDC2_SDCARD_DET	B-PD:nppukp	

76	GND	GND	GND
77	RF_WIFI/BT	AI,AO	RF signal
78	GND	GND	GND
79	CAM0_RST_N	DO;B-PD:nppukp	Rear camera reset; Configurable I/O
80	CAM0_PWDN	DO;B-PD:nppukp	Rear camera pwn Configurable I/O
81	CAM1_RST_N	DO;B-PD:nppukp	Front camera reset Configurable I/O
82	CAM1_PWDN	DI;B-PD:nppukp	Front camera pwn Configurable I/O
83	CAM_I2C_SCL	B-PD:nppukp	Camera I2C_SCL,can't be used for other
84	CAM_I2C_SDA	B-PD:nppukp	Camera I2C_SDA,can't be used for other
85	GND	GND	GND
86	GND	GND	GND

117	GPIO10_SPI_CS	B-PD:nppukp	Configurable I/O SPI or I2C
118	GPIO9_SPI_MISO	B-PD:nppukp	Configurable I/O SPI
119	GPIO8_SPI_MOSI	B-PD:nppukp	Configurable I/O SPI
120	GND	GND	GND
121	RF_GPS	AI	RF signal-GPS ANT
122	GND	GND	GND
123	GPIO31	B-PD:nppukp	Configurable I/O,
124	GPIO69	B-PD:nppukp	Configurable I/O,
125	VREG_L6_1P8	OUPUT	PMU Supply 1.8V
126	VCOIN	PI	VCOIN
127	CHARGE_SEL	DI	SMB CHAGER SEL
128	PM8916_MPP2	AO-Z;DI, DO	Configurable MPP; used for PWM or ADC
129	VREG_L17_2P85	OUPUT	PMU Supply 2.85V
130	GND	GND	GND
131	RF_DIV	AI	RF signal-DIV ANT
132	GND	GND	GND
133	VBATT_SNS	PI	Battery voltage sense point
134	BAT_THERM	DI	Battery therm monitor
135	GND	GND	GND
136	CDC_HPH_R	AO	Earphone right output
137	CDC_HPH_REF	AI	Earphone driver amplifier ground reference
138	CDC_HPH_L	AO	Earphone left output
139	CDC_HS_DET	DI	Headset detection
140	GND	GND	GND
141	VBUS	PI	USB VBUS Voltage
142	VBUS	PI	USB VBUS Voltage
143	GND	GND	GND
144	GND	GND	GND
145	VBAT	PI,PO	Battery
146	VBAT	PI,PO	Battery
147	GND	GND	GND
148	GND	GND	GND
149	GND	GND	GND
150	GND	GND	GND
151	RESERVED		RESERVED
152	RESERVED		RESERVED
153	RESERVED		RESERVED
154	RESERVED		RESERVED



199	RESERVED		RESERVED
200	GND	GND	GND
201	GND	GND	GND
202	RESERVED		RESERVED

# Q 5

